

Amendment to the Specification:

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Rewrite the carry-over paragraph from page 13 to page 14 to read as follows:

21
To overcome this problem, the present invention provides a buffer management device 304 301 having an equalizer filter 601 designed to monitor the movement of the coefficients within a tracking buffer 604. An example of a length-32 tap digital filter according to the invention is illustrated in Figure 6. At time $t(0)$, the data filter buffer 602 and the coefficient filter buffer 603, each of length 32, are used to compute the filter output. The data filter buffer 602 and the coefficient filter buffer 603 can be centered inside the much longer buffers, such as the tracking buffers 604. ~~Alternatively~~ Alternatively, the coefficient filter buffer 603 and the data filter buffer 602 can be separate buffers which point to the locations of the set of coefficients, represented by 603, and the set of data, represented by 602, which are to be used in the equalizer filter. In the example, the tracking buffers are of length 128. The coefficients 605 outside the 32 centered coefficients 605 are initialized to zero. As described above, the filter output of either the filters of the in-phase equalizer 302 or quadrature equalizer 303 can be

represented as
$$y(n) = \sum_{k=0}^{N-1} w[k]x[n-k].$$